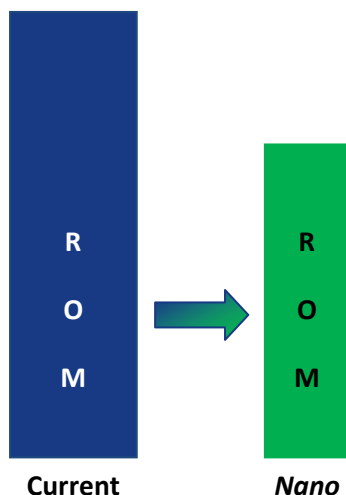


NovoBlox[®] nano



Features

- 100% Reliability - No Tail Bit Failures
- 30+ years Data Retention
- No External Charge Pump
- DONE Programming Signal
- Industry Leading Performance
- Access times <8nsec
- Foundry and Process Independent
- Low Programming Current < 15 mA

Product Description

Novocell's NovoBlox[®] one-time programmable (OTP) memory IP is a non-volatile memory block which can be embedded in standard Logic CMOS without any additional process steps or post processing. It has the ability to be programmed at wafer level or in package.

NovoBlox *Nano* is a new high density version of the reliability leading NovoBlox OTP. The *Nano* product line features high density blocks (8K-256K bits) with a 70% smaller footprint than the original NovoBlox OTP. Like the original NovoBlox OTP, NovoBlox *Nano* programs dynamically until all bits are programmed, ensuring that there are no tail bit failures.

Programming does not require an external charge pump. NovoBlox SmartBit™ technology generates and confines the breakdown voltage entirely within the memory core. Unprogrammed cells are not subjected to voltage outside the native process parameters and have the same reliability as the underlying CMOS technology. High voltage during programming is applied until the current signature of hard breakdown is detected, guaranteeing 100% programming and data retention. NovoBlox is silicon proven at leading fabs including, IBM, TowerJazz, TSMC, UMC, GlobalFoundries, and SilTerra.

Product Specifications

Custom aspect ratios can be provided. Please visit www.novocellsemi.com for more information.

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